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) (no. 5)						Customized PT0	O/SB/21 (07-06)	
**/		Applicati	on#	10/828,983				
ACCULATION		Ţ	Confirmati	on#	6909			
TRANSMITTAL FORM			Filing	Date	04/21/2004			
			First Inve		RAPAICH, MARK			
(for all correspondence after initial filing)			Art	Unit	2114			
		Exan	niner	SCHELL, JOSEPH O.				
Total number of pages in this submission =			Docl	ket#	P1997US00			
					(P09121US00/RFH)			
ENCLOSURES (check all that apply)								
Fees calculated below Request For Reconside including Attachment After Final Amendment including Attachment Extension of Time Petiti	Response to Missing Parts/Incomplete Appl. Certified Copy of Priority Document(s) Information Disclosure Statement Drawing(s) Terminal Disclaimer							
	NOW		usly Paid For		nt Extra	Rate	\$	
☐ TOTAL CLAIMS	28	7 70010	28		0	X \$ 50 =	_	
☐ INDEPENDENT CLAIMS	·		4		0	X \$ 200 =		
TOTAL OF ABOVE CLAIMS FEES =								
Reduction by ½ for small entity status of applicant								
SUBTOTAL =								
Fee for extension of time (per attached Petition)								
Other fee for								
TOTAL OF ALL FEES = 0						0		

The Commissioner is hereby authorized to charge the above-noted fee of \$ to Deposit Account No. 50-0439.

X The Commissioner is authorized to charge any fee, additional fee or extension fee due in connection herewith to Deposit Account No. 50-0439:

(1) if no payment or an insufficient payment is enclosed and a fee is due in connection herewith; or

(2) if no petition for extension of time is enclosed but an EOT is required - and in this event, applicant hereby petitions under 37 CFR 1.136(a) for an extension of time of as many months as are required to render this submission timely. The Commissioner is hereby authorized to charge any such fee to

Deposit Account No. 50-0439

Date: June 13, 2007

By: Ross F. Hunt, Jr. Registration No.: 24,082

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REQUEST FOR RECONSIDERATION

	Application #	10/828,983
	Confirmation #	6909
	Filing Date	04/21/2004
	First Inventor	RAPAICH, MARK
	Art Unit	2114
•	Examiner	SCHELL, JOSEPH O.
	Docket #	P1997US00 (P09121US00/RFH)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

In response to the Office Action dated April 19, 2007, reconsideration of the rejection of the claims is respectfully requested.

Claims 1 - 7, 9 - 15, 17 - 21 and 23 - 28 have been rejected under 35 U.S.C. 102(b) as being anticipated by Lin (U.S. Patent No. 6,862,695). This rejection is respectfully traversed.

Claims 1, 11, 19 and 26 are the independent claims of the application.

Claim 1 recites an apparatus for detecting and indicating faults on a computer motherboard comprising, *inter alia*, a visual indicator coupled to and controlled by a microprocessor for providing a visual indication when a fault on the computer motherboard is detected during execution of diagnostic instructions by the microprocessor.

It is suggested in the Office Action that Lin discloses an apparatus having such a visual indicator coupled to and controlled by a microprocessor for providing a visual indication when a fault on a computer motherboard is detected during execution of diagnostic instructions by the microprocessor "as shown in Figure 5a steps 504 and 505."

Steps 504 and 505 of Figure 5a of the Lin reference are "Failure Encountered In RAM's Test Procedure?" and, if yes, "Blink LED ON And OFF At a First Frequency," respectively. The Lin reference also discloses that the LED is connected to an I/O port of a bridge chip. More particularly, Lin provides that:

"If the model name and the specification information of the RAM 32 can not be correctly obtained, the BIOS program